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T Anderson, Y Breitbart, HF Korth, A Wool - Proceedings of the 1998 ACM SIGMOD International ..., 1998 - portal.acm.org

... Figure 1 depicts a **state-transition** diagram for the pro ... If Ti submits a **read** or **write** operation at ... In pro- cessing operations, maintain the **data access** set6 of ...

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The MIT Alewife machine: A large-scale distributed memory multiprocessor - ► kfupm.edu.sa [PDF]

A Agarwal, D Chaiten, K Johnson, D Kranz, J ... - Scalable shared memory multiprocessors, 1991 - books.google.com

... P={kkk 12 n Figure 2: Directory **state transition** diagram. ... by one processor and then

read by all ... for every cycle it spends servicing an application **data access** ...

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P Stenström, M Brorsson, L Sandberg - ACM SIGARCH Computer Architecture News, 1993 - portal.acm.org

... the recommended mechanism to support shared **data access**. ... Last-writer pointer Figure

4: **State-transition** raph for ... The latter can reduce **read** and **write** penalty ...

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D Yeung, J Kubiatowicz, A Agarwal - ACM SIGARCH Computer Architecture News, 1996 - portal.acm.org

... computing a cliff, and the **read-write** copy is ... consistent, or that is stronger than **release consistency**. ... Figure 4 shows the **state transition** diagram for these ...

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JL Baer, TF Chen - portal.acm.org

... very effective for reducing the **data access** penalty for ... **Write** buffers, stream

buffers[5], and lockup-free caches [6] are three mechanisms that allow the over- ...

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Coordination for Internet application development

A Ormicini, F Zambonelli - Autonomous Agents and Multi-agent systems, 1999 - Springer

... bound by the built-in **data-access** mechanisms provided ... to specifying a new **state transition** in response ... ii) manipulate terms, and (iii) **read**, **write** and consume ...

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TF Chen, JL Baer - IEEE transactions on computers, 1995 - cecs.pdx.edu

... basic scheme, Le., one without **state transition** mechanism. ... program analysis to detect

regular **data access** patterns ... prefetch instructions for both **read** and **write** ...

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JL Carroll, DDE Long, JF Paris - Proc. of the 7th Int. Conf. on Distr. Computing Systems - cse.ucsd.edu

... executes the **consistency** control and **data access** algorithms. ... on the local site, then

the **read** operation can ... From the **state transition** diagram, we have kkpk = (n ...

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B Westfechtel - Integrated Computer Aided Engineering, 1996 - citeseer.nus.edu.sg

... the CIM Manager itself cannot perform **consistency** control on ... Freeze Thaw **Read**, **Write**

Read ... Figure 6 **State transition** diagrams for revisions and configurations ...

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Z Chishti, MD Powell, TN Vijaykumar - Computer Architecture, 2005, ISCA'05. Proceedings. ... , 2005 - ieeexplore.ieee.org

... CMP-NuRAPID (1) uses sequential tag-**data access**; (2) divides the ... Figure 4a shows

the **state transition** diagram for a 4 ... from I to C on a **read/write** miss requires ...

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"state transition" "data access" read

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